The exemplary table 1350 in Figure 13B shows how the startup source file vector names 1351 map to the data sheet interrupt names 1352 and to fixed and [PSoC] programmable system block (configurable) interrupts 1353. For example, interrupt 05 maps to digital [PSoC] programmable system block 410a "DBA03". Furthermore, it is of type [PSoC] programmable system block. Referring to Figure 13A, a ljmp to the counter_16 ISR has been automatically placed in the vector table 1300 at the location for [PSoC] programmable system block DBA03. Referring now to Figure 1B, the ljmp was placed automatically to reflect the configuration having module 304 CNTR16_MSB at digital block [410b] 410a "DBA03". Other interrupt vectors have been automatically added to the interrupt vector table 1300, as well.

Please replace the paragraph beginning at page 28, line 9 with the following new paragraph:

Automatic generation of datasheets is described in co-pending US patent application serial number [_____] 09/994,600, filed concurrently herewith, entitled "SYSTEM AND METHOD FOR DYNAMICALLY GENERATING A CONFIGURATION DATASHEET," by Ogami et al., attorney docket number CYPR-CD01174M and assigned to the assignee of the present invention and incorporated herein by reference.

IN THE ABSTRACT

Please replace the Abstract beginning at page 38, line 4 with the following new Abstract:

A method to facilitate circuit design. First, a schematic and data sheet for a selected module may be displayed. Next, in response to a request for a position for the module among available resources (e.g., [PSoC] programmable system blocks), a potential position for the module is computed. The position is displayed on a graphical user interface by mapping the module to one or more [PSoC] programmable system blocks. Additional user modules may then be selected and placed. After allowing the user to configure the circuit by selecting circuit parameters and pin-outs,

various items are automatically generated to facilitate programming the target device. For example, application programming interfaces (APIs) for programming an operation of the modules, source code for realizing the modules in the resources, an interrupt vector table, and a data sheet for the circuit may be automatically generated.

SUPPORT FOR AMENDMENTS

Support for the amendments herein can be found throughout the specification (e.g., page 1, lines 3-4; page 4, lines 12-17), Title and Abstract as originally filed, and in the copending applications cited in the specification. The present amendment intends to remove references to the trademarks of Cypress MicroSystems, Inc. (see, e.g., M.P.E.P. § 608.01(v) and the attached printouts from http://tess.uspto.gov/, notably the "PSOC" trademark registration information therein, and http://www.cypressmicro.com/corporate/CY_Announces_nov_13_2000.html). No new matter is introduced.